



2005



1997 6



1998



1999 6



1999 6 2000 2



20

921



2005 8 2007 12



94



2008 1 2009 12



2010



60

The diagram illustrates a 64-bit bus system. It features six horizontal rows of 64-bit registers. The first five rows are full-length, each containing 64 small rectangular cells. The sixth row is shorter, containing only 32 cells. Below the registers, a block labeled 'PCP' is shown, connected to the bus. The label 'PCP' is positioned to the right of the shorter register row.

ISP

[illegible]

--	--	--	--	--	--

()

1



□□□□□□□□□□





Revision #4

Created 10 December 2025 03:12:17 by 1944

Updated 15 December 2025 05:12:15 by 1944