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The diagram illustrates a 1000-bit packet structure. It is divided into two main sections: a 100-bit header (IHF) and a 900-bit body (SOP). The header is represented by a single row of 100 small squares. The body is represented by 9 rows of 100 small squares each. The total number of squares is 100 + 900 = 1000.


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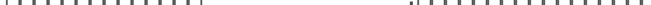




Diagram illustrating the instruction format (32 bits):

- Opcode (26 bits)
- Register (6 bits, labeled CHU)
- Immediate (10 bits)