



Diagram illustrating a 32-bit register structure. The register is divided into seven segments, each represented by a row of boxes. The first segment contains 32 boxes. The second segment contains 16 boxes, followed by a gap, then a box labeled '31', a gap, and a box labeled '32'. The third segment contains 32 boxes. The fourth segment contains 32 boxes. The fifth segment contains 32 boxes. The sixth segment contains 32 boxes. The seventh segment contains 16 boxes.

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 (4)